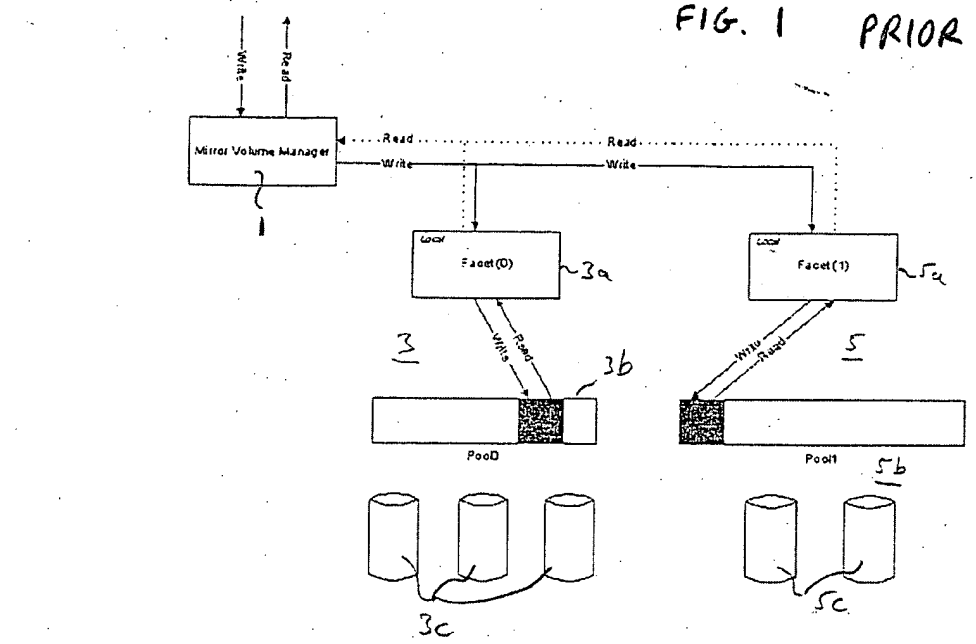
FIG. 1 PRIOR ART



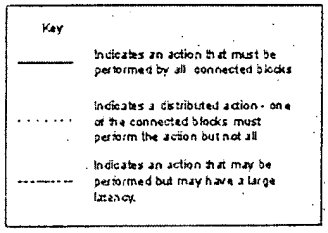


FIG. 2

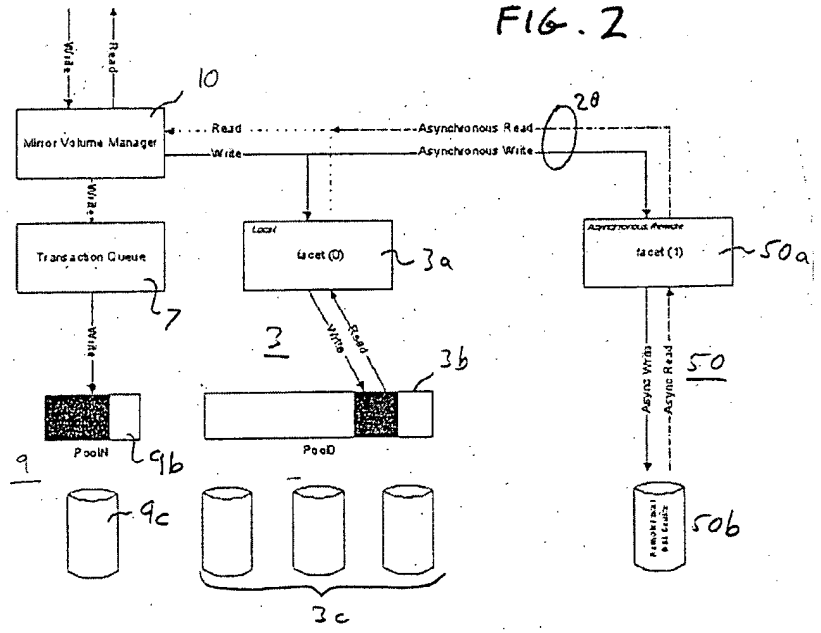


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FIG. 3

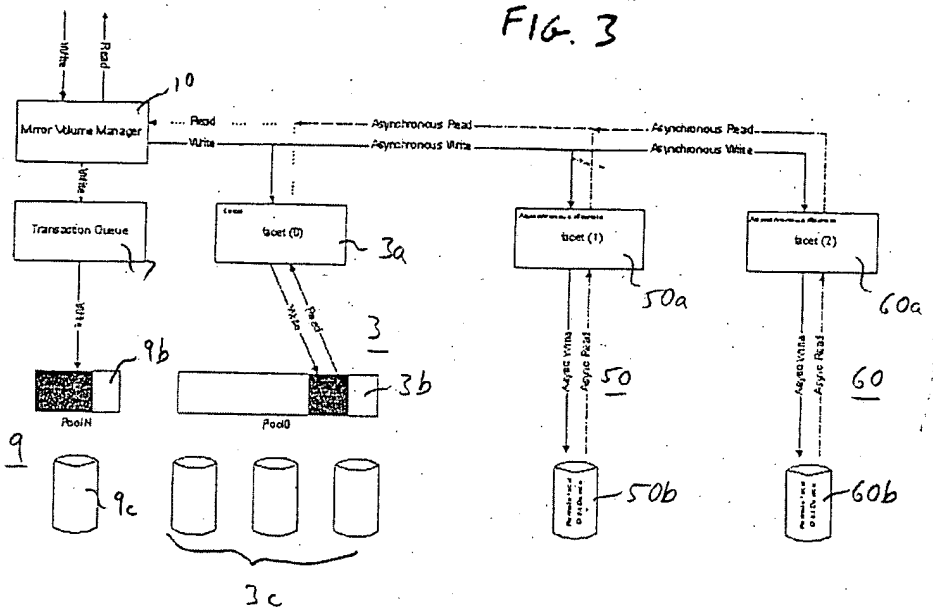

FIG. 5

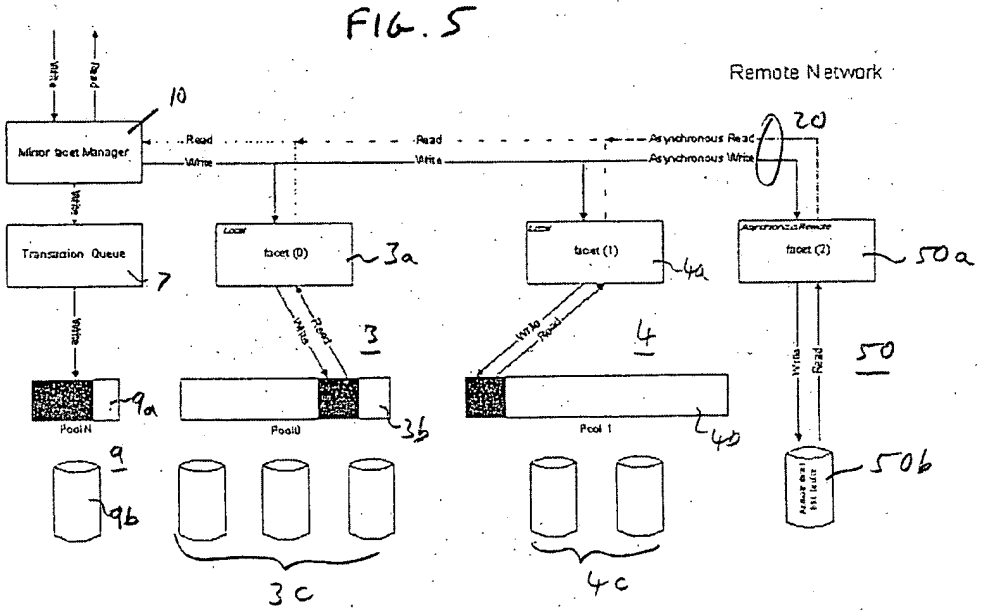

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FIG. 4

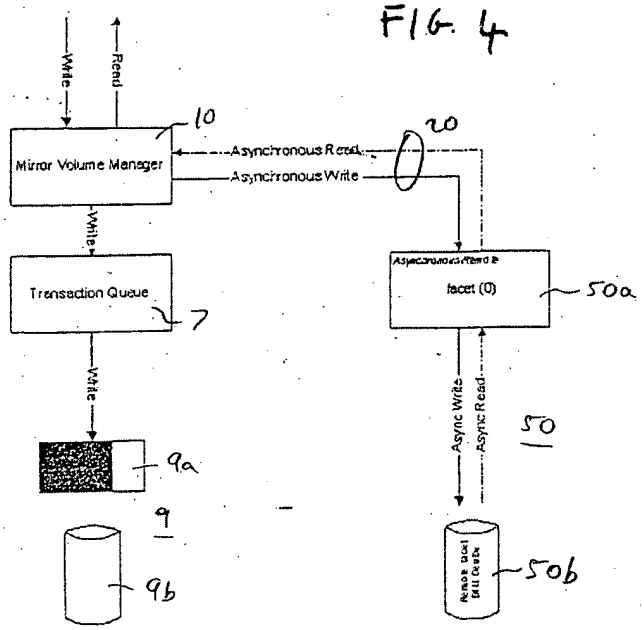
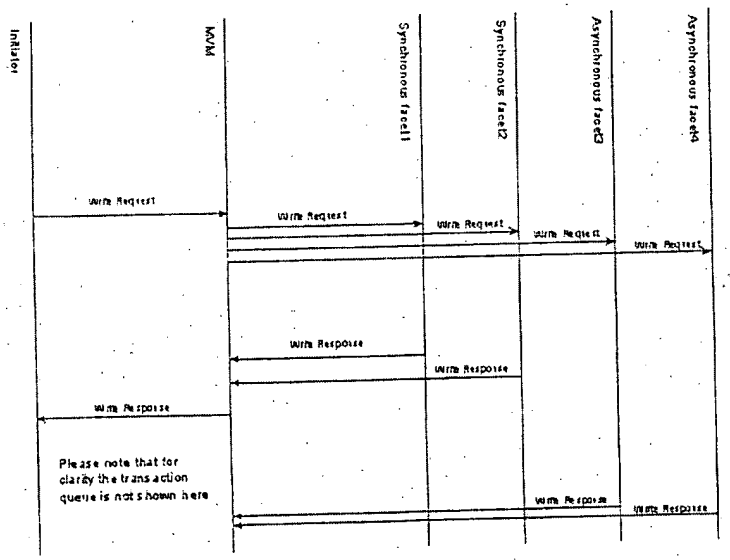
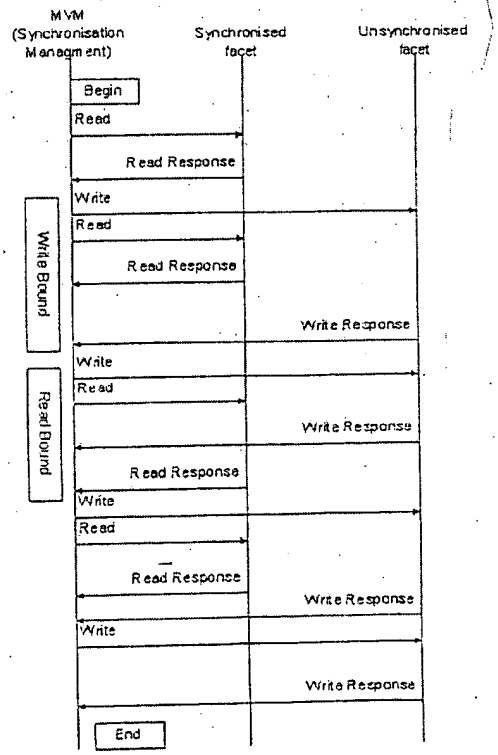


FIG. 6



Synchronous and asynchronous write message sequence

FIG. 7



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